

252997

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
(Case No. P043D2C2C)

3/P. And+B

In the Application of:)
)
 FARMWALD ET AL.) Group
) Art Unit:
 Serial No: Continuation of 09/196,199)
)
 Filed: FEBRUARY 19, 1999) Before
) Examiner:
)
 Title: INTEGRATED CIRCUIT I/O USING A)
 HIGH PERFORMANCE BUS INTERFACE)

Assistant Commissioner for Patents
Washington, DC 20231

PRELIMINARY AMENDMENT

Dear Sir:

Prior to the examination of the above-referenced application,
kindly amend the application as follows:

IN THE TITLE:

Please delete the title and substitute --METHOD OF OPERATING
A MEMORY HAVING A VARIABLE DATA OUTPUT LENGTH AND A PROGRAMMABLE
REGISTER--.

IN THE SPECIFICATION:

On page 3, line 9, delete "micro-processor" and substitute
--microprocessor--.

On page 6, line 1, delete "4,646,279" and substitute
--4,646,270--.

A